



Docket No.: 43889-937

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277
Sadami Takeoka et al. : Confirmation Number: 3437
Serial No.: 09/843,687 : Group Art Unit: 2825
Filed: April 30, 2001 : Examiner: DO, THUAN V
For: METHOD OF DESIGNING SEMICONDUCTOR INTEGRATED CIRCUIT

RESPONSE TO RESTRICTION REQUIREMENT

Mail Stop Restriction Requirement
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Noting the Office Action of March 8, 2005 wherein restriction has been required, Applicant(s) hereby elect **Group II**, specified as **claims 2, 5, 8 and 12**, for prosecution in the above-identified application.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,
MCDERMOTT, WILL & EMERY

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Date: **April 8, 2005**